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REPLY UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2829

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6/20/05

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/620,944

Filed: July 16, 2003

Inventor(s):

Welbon et al.

§ Examiner: Paresh H. Patel  
§ Group/Art Unit: 2829  
§ Atty. Dkt. No: 5681-66200

Title: Test System Including an Apparatus For Conveying Signals Between a First Circuit Board and a Second Circuit Board

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Erik A. Heter

Printed Name

Signature

June 6, 2005

Date

**AMENDMENT; RESPONSE TO OFFICE ACTION OF  
April 6, 2005**

**ATTN: BOX AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of April 6, 2005, to further highlight why the application is in condition for allowance.